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Page 1 of 329. CORRECTIONS TO SOLUTIONS MANUAL In the new edition, some chapter problems have been reordered and equations and figure refer- ences have changed.

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EE 215A HO#1 Fall 2014 Analog Integrated Circuit Design Instructor: Behzad Razavi 56-147D, Eng. IV

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Why Analog?  $\hat{a} \in \phi$  Most of the physical signals are analog in nature!  $\hat{a} \in \phi$  Although digital is great we need an analog interface to convert physical signals from analog to digital  $\hat{a} \in \phi$  Also, in some application after processing the signals in digital domain, we need to convert them back to analog.

# **EECE488: Analog CMOS Integrated Circuit Design**

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Electronic noise – estimates and countermeasures Design of Analog CMOS integrated circuits, Bahzad Razavi, Chapter 7 - Noise • Thermic noise: – Represented as parallel current at the output. Using γ=2/3. (For small linewidths, 250nm and ... • Vn,out: Integrated output noise with load capacitance CL

#### INF 5460 Electronic noise â€" estimates and countermeasures

Design of Analog CMOS Integrated Circuits Behzad Razavi Errata p. 6, Section 1.3, ﬕrst sentence should read: The idea of metal-oxide-semiconductor ... p. 26, In Eq. (2.28), move 1 + V DS from the denominator to the numerator. p.32, ﬕfth line from top: change 4Φ F to 4 j Φ . p. 88, second linebelow Eq. (3.124) should read: ... roughly g m r 2 O.

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#### Where can I find the solution manual of RF

A. Manian and B. Razavi, "A 32-Gb/s 9.3-mW CMOS Equalizer with 0.73-V Supply,― Proc. IEEE Custom Integrated Circuits Conference, Sept. 2014. B. Razavi, " The Role of Translational Circuits in RF Receiver Design ,― Proc. IEEE Custom Integrated Circuits Conference, Sept. 2014.

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EECE488: Analog CMOS Integrated Circuit Design Introduction and Background Shahriar Mirabbasi Department of Electrical and Computer Engineering University of British Columbia shahriar@ece.ubc.ca Technical contributions of Pedram Lajevardi in revising the slides is greatly acknowledged.

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